## Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination GEORGE ET AL. | Examiner | Art Unit | Heather A. Doty | 2813 | Page 1 of 1

## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,763,328	06-1998	Yoshihara et al.	438/725
	В	US-			
	ပ	US-			
	D	US-			
	Е	US-			
	F	US-			
	G	US-			
	H	US-			
	_	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number - Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					***************************************
	0					-
	P					
	Q					
	R					
	S					 
	Т					 

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Wolf, et al. Silicon Processing for the VLSI Era, Lattice Press, 2000, vol. 1, p. 708				
	V					
	w					
	x					

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.